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Erfinder: DATAKE KENJI

Anmelder: TOSHIBA CORP

Titel: ORTHOGONAL TRANSFORM CIRCUIT

Zusammenfassung

PURPOSE: To reduce the number of required bits of an orthogonal transform circuit to miniaturize the circuit by subjecting a signal to nonlinear compression after orthogonal transform.

CONSTITUTION: 8-bit picture data inputted from an input terminal 1 is orthogonally transformed by an orthogonal transform circuit 2, and the output is nonlinearly compressed by a nonlinear compressing circuit 3. Though 10 or more bits are required conventionally for the purpose of obtaining a picture quality free from visual degradation, the picture quality is not degraded regardless of 8 bits because nonlinear compression is performed; and the memory size of a transposing circuit 4 in the next stage is miniaturized, and an orthogonal transform circuit 5 following the circuit 4 is reduced. The output of the orthogonal transform circuit 5 is nonlinearly compressed again by a nonlinear compressing circuit 6. Though 12 bits are required conventionally for the purpose of obtaining pictures of good quality, 8 bits suffices for output data from an output terminal 7 because nonlinear compression is performed twice.

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